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U.S. UTILITY Patent Application

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PATENT NUMBER and  
ISSUE DATE

APPL NUM 10061546	FILING DATE 02/01/2002	CLASS 257174	SUBCLASS 262	GAU 2827	EXAMINER N. H. K. L.
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**\*\*APPLICANTS:** Sun Xiao-Peng; Wang Nanlei;

**\*\*CONTINUING DATA VERIFIED:**  
THIS APPLICATION IS A CIP OF 09/675,086 09/28/2000

**\*\* FOREIGN APPLICATIONS VERIFIED:**

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	017829-000710US
Verified and Acknowledged Examiners's initials	
TITLE : Multi-layer interconnect module and method of interconnection	
U.S. DEPT. OF COMMERCE/PAT & TM-PTO 436 (Rev. 12-94)	

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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